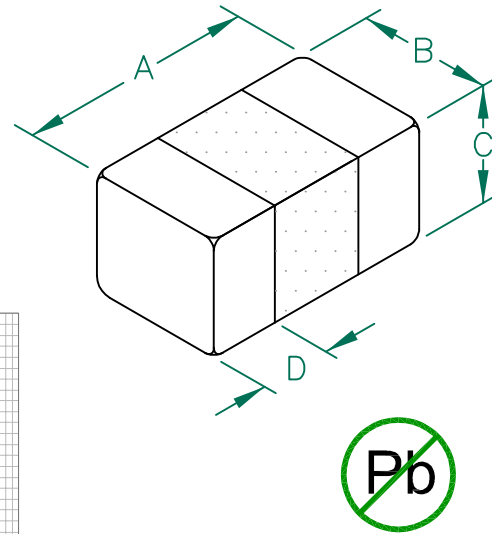


# LI0402E750R-10

**UNCONTROLLED DOCUMENT**

## PHYSICAL DIMENSIONS:

A	1.01 [.040]	+ .18 [.007]
B	0.50 [.020]	+ .20 [.008]
C	0.50 [.020]	+ .20 [.008]
D	0.30 [.012]	+ MAX.



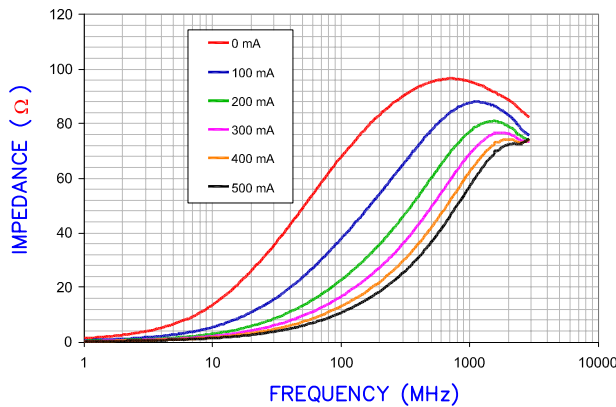
## ELECTRICAL CHARACTERISTICS:

Z @ 100MHz ( $\Omega$ )	DCR ( $\Omega$ )	Rated Current
Nominal	75	
Minimum	56	
Maximum	94	0.10 500 mA

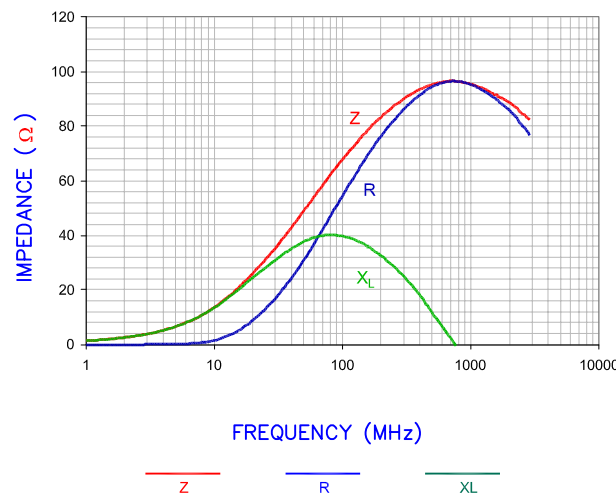
NOTES: UNLESS OTHERWISE SPECIFIED

1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 10,000 PCS/REEL, PAPER TAPE.
2. TERMINATION FINISH IS 100% TIN.
3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
4. OPERATING TEMP. RANGE:  $-40^{\circ}\text{C} \sim +125^{\circ}\text{C}$ . (INCLUDING SELF-HEATING)

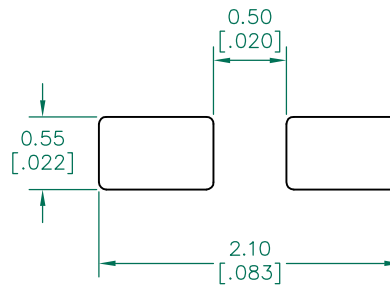
Z vs. FREQUENCY  
IMPEDANCE UNDER DC BIAS



|Z|, R, AND XL vs. FREQUENCY

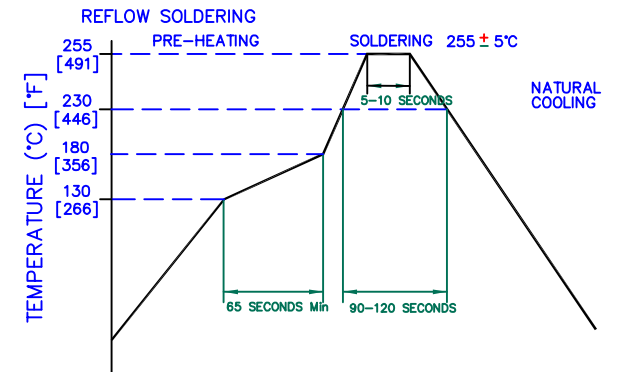


## LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.762 [.030] to this dimension.)

## RECOMMENDED SOLDERING CONDITIONS



DIMENSIONS ARE IN mm [INCHES].				This print is the property of Laird Tech. and is loaned in confidence subject to return upon request and with the understanding that no copies shall be made without the written consent of Laird Tech. All rights to design or invention are reserved.				
				PROJECT/PART NUMBER:		REV	PART TYPE:	DRAWN BY:
				LI0402E750R-10		B	CO-FIRE	JRK
				DATE: 11/20/08		SCALE: NTS		SHEET:
				CAD # LI0402E750R-10-B		TOOL # -		2 of 2
REV	DESCRIPTION	DATE	INT					