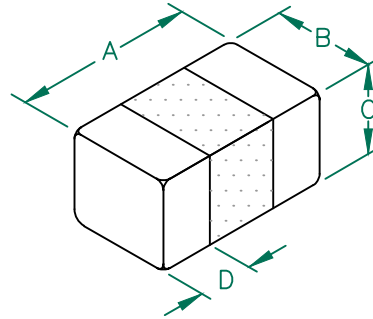


LI0402B800R-10

PHYSICAL DIMENSIONS:

A	1.01 [.040]	+	0.18 [.007]
B	0.50 [.020]	+	0.20 [.008]
C	0.50 [.020]	+	0.20 [.008]
D	0.30 [.012]	+	MAX.



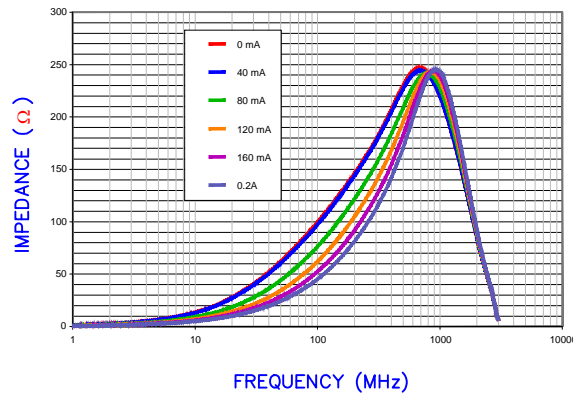
ELECTRICAL CHARACTERISTICS:

Z @ 100MHz (Ω)	DCR (Ω)	Rated Current
Nominal	80	
Minimum	60	
Maximum	100	200 mA

NOTES: UNLESS OTHERWISE SPECIFIED

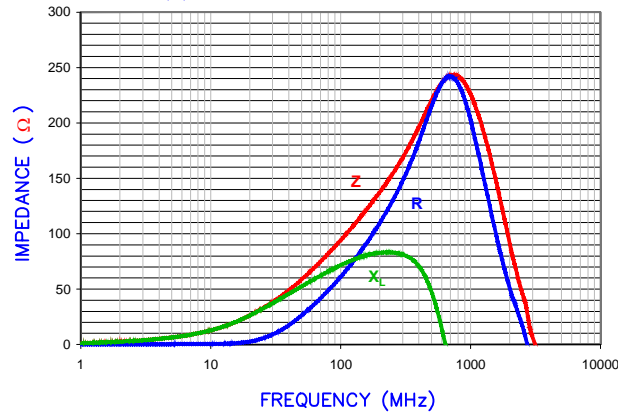
1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 10,000 PCS/REEL, PAPER TAPE.
2. TERMINATION FINISH IS 100% TIN.
3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
4. OPERATING TEMP. RANGE: $-40^{\circ}\text{C} \sim +125^{\circ}\text{C}$. (INCLUDING SELF-HEATING)

Z vs. FREQUENCY
IMPEDANCE UNDER DC BIAS



FREQUENCY (MHz)

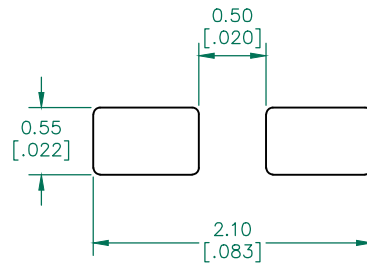
|Z|, R, AND XL vs. FREQUENCY



FREQUENCY (MHz)

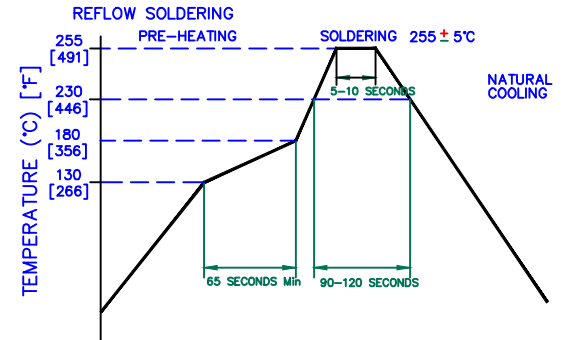
— Z — R — XL

LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.762 [.030] to this dimension.)

RECOMMENDED SOLDERING CONDITIONS



DIMENSIONS ARE IN mm [INCHES].				This print is the property of Laird Tech. and is loaned in confidence subject to return upon request and with the understanding that no copies shall be made without the written consent of Laird Tech. All rights to design or invention are reserved.			
D	ADD OPERATING TEMPERATURE UPDATE LAIRD LOGO AND REFLOW CURVE	08/05/13	QU	PROJECT/PART NUMBER: LI0402B800R-10		REV	D
C	CORRECT Z SPECS	09/11/08	JRK	DATE: 05/10/06		SCALE:	NTS
B	UPDATE COMPANY LOGO	07/08/08	JRK	CAD #		SHEET: 1 of 1	
A	ORIGINAL DRAFT	05/10/06	JRK	TOOL #			
REV	DESCRIPTION	DATE	INT	LI0402B800R-10-D			

