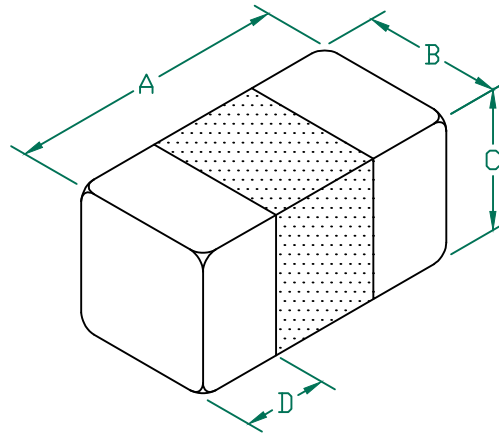


HZ0402C601R-10

PHYSICAL DIMENSIONS:

A	1.00 [.040]	+	0.05 [.002]
B	0.50 [.020]	+	0.05 [.002]
C	0.50 [.020]	+	0.05 [.002]
D	0.30 [.012]		MAX.



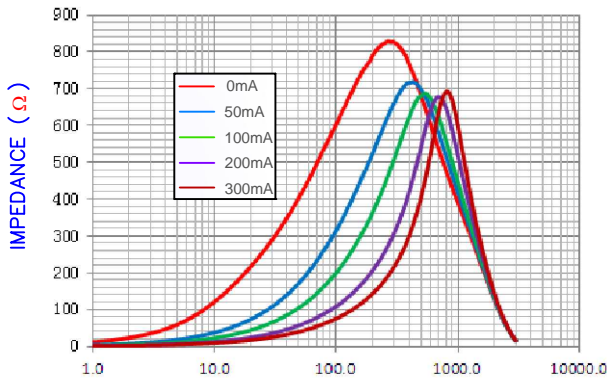
ELECTRICAL CHARACTERISTICS:

	Z @ 100MHz (Ω)	DCR (Ω)	Rated Current
Nominal	600		
Minimum	450		
Maximum	750	0.60	300 mA

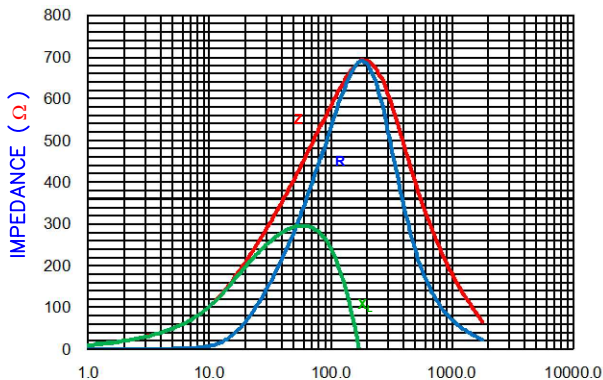
NOTES: UNLESS OTHERWISE SPECIFIED

1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 10,000 PCS/REEL, PAPER TAPE.
2. TERMINATION FINISH IS 100% TIN.
3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
4. OPERATING TEMPERATURE TEMP: -40°C~+125°C (INCLUDING SELF-HEATING)
5. COSMETIC SPECIFICATION REFER TO WI-QA-124.

Z vs FREQUENCY
IMPEDANCE UNDER DC BIAS



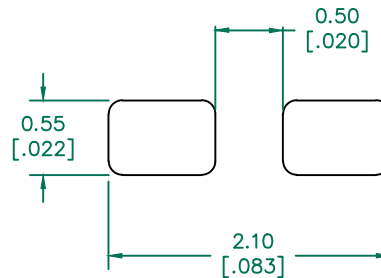
FREQUENCY (MHz)
|Z|, R, AND X vs. FREQUENCY



Z R X_L

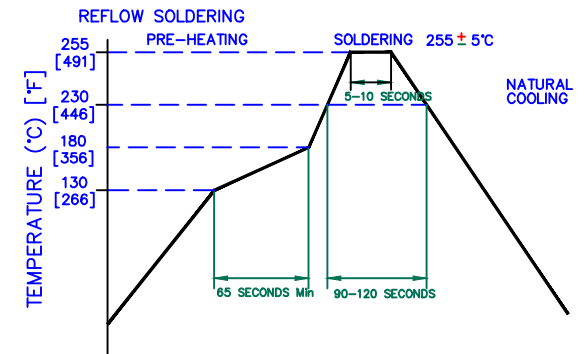
AGILENT E4991A RF Impedance/Material Analyzer
HP 16194A Test Fixture. TEST REF. 3228

LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.763
(.030 to this dimension.)

RECOMMENDED SOLDERING CONDITIONS



RoHS

DIMENSIONS ARE IN mm [INCHES].				This print is the property of Laird Tech. and is loaned in confidence subject to return upon request and with the understanding that no copies shall be made without the written consent of Laird Tech. All rights to design or invention are reserved.			
				Laird			
PROJECT/PART NUMBER:				REV	PART TYPE:	DRAWN BY:	
C	UPDATE LAIRD LOGO AND NOTES 4	08/05/13	QU	HZ0402C601R-10	C	CO-FIRE	WEI
B	UPDATE REFLOW ADD DC BIAS CURVE ADD OPERATING TEMPERATURE	11/21/12	QU	DATE: 11/21/12	SCALE: NTS	SHEET: 1 of 1	
A	ORIGINAL DRAFT	03/01/11	WEI	CAD #	TOOL #		
REV	DESCRIPTION	DATE	INT	HZ0402C601R-10-C			