HR2220P601R-10

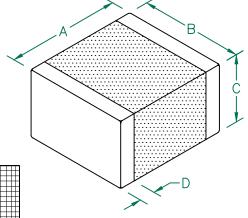
PHYSICAL DIMENSIONS:

A 5.59 [.220] \pm 0.51 [.020]

B 5.08 [.200] ± 0.25 [.010]

C 3.05 [.120] ± 0.25 [.010]

D 0.76 [.030] ± 0.25 [.010]

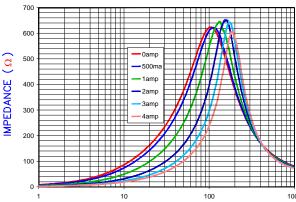


ELEC1	ELECTRICAL CHARACTERISTICS:									
Z @ 100MHz (Ω)		DCR $\left(\begin{array}{c}\Omega\end{array}\right)$	Rated Current							
Nominal	600									
Minimum	450									
Maximum	750	0.025	4000 mA							

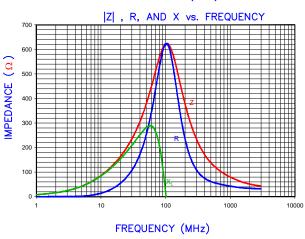
NOTES: UNLESS OTHERWISE SPECIFIED

- 1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 13" REELS, 2,000 PCS/REEL.
- 2. U.S. PATENTS 5,821,846, 6,107,907, AND 6,249,205B1 ARE APPLIED
- 3. TERMINATION FINISH IS 100% TIN.
- 4. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
- 5. OPERATEING TEMPERATURE TEMP: -40°C~+125°C (INCLUDING SELF-HEATING)
- 6. COSMETIC SPECIFICATION REFER TO WI-QA-124.

Z vs FREQUENCY IMPEDANCE UNDER DC BIAS

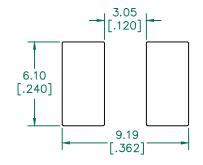


FREQUENCY (MHz)



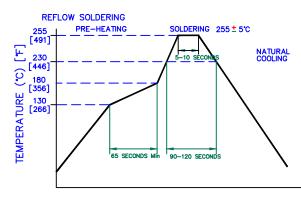
AGILENT E4991A RF Impedance/Material Analyzer AGILENT 16194A Test Fixture. TEST REF. 3298

LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.762 [.030] to this dimension.)

RECOMMENDED SOLDERING CONDITIONS





	DIMENSIONS ARE IN mm [INCHES]. This print is the property of Lair				ď					
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ı					rights to design or invention are reserved.	!				_
İ	D	UPDATE NOTES 2	05/18/15	QU	PROJECT/PART NUMBER:	\neg	REV	PART TO	PE:	DRAWN BY:
Ī	С	ADD OPERATING TEMPERATURE UPDATE LAIRD LOGO AND REFLOW CURVE	08/05/13	QU	HR2220P601R-10				-FIRE	QU
ŀ	В	LIBRATE COMPANY LOCO ADD BOHS	02/14/08	IDV	DATE:	SCA	I F.		SHEET:	
L	ם				1 08/05/13		"NTS		andt II	
	Α	ORIGINAL DRAFT	04/03/04	TMB	CAD #	TOO	L #	-	1	of 1
Ī	REV	DESCRIPTION	DATE	INT	HR2220P601R-10-D				· '	01 1