HI2220P171R-10

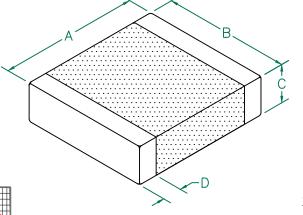
PHYSICAL DIMENSIONS:

A 5.59 [.220] ± 0.51 [.020]

B 5.08 [.200] ± 0.25 [.010]

C 1.52 [.060] ± 0.25 [.010]

D 0.76 [.030] ± 0.25 [.010]



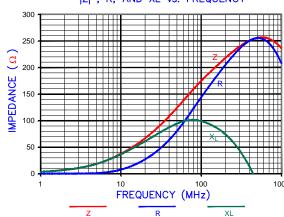
ELECT	ELECTRICAL CHARACTERISTICS:									
Z @ 100MHz (Ω)		DCR (Ω)	Rated Current							
Nominal	170									
Minimum	128									
Maximum	213	0.030	4000 mA							

NOTES: UNLESS OTHERWISE SPECIFIED

- 1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 13" REELS, 2,000 PCS/REEL.
- 2. TERMINATION FINISH IS 100% TIN.
- 3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
- 4. OPERATEING TEMPERATURE TEMP: -40°C~+125°C (INCLUDING SELF-HEATING)

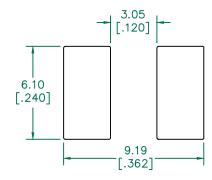
Z vs. FREQUENCY IMPEDANCE UNDER DC BIAS 300 250 -0amp -1 amp -2 amp -2 amp -3 amp -4 amp -4 amp -50 -1000 FREQUENCY (MHz)

|Z| , R, AND XL vs. FREQUENCY



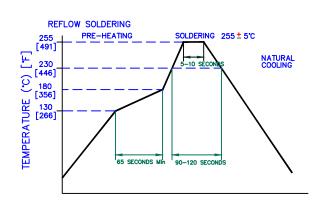
AGILENT E4991A RF Impedance/Material Analyzer HP 16092A Test Fixture. TEST REF. 3185

LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.762 [.030] to this dimension.)

RECOMMENDED SOLDERING CONDITIONS



[DIMENSIONS ARE IN mm [INCHES]. This print is the property of Laird									
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					copies shall be made without the written consent of Laird Tech. All rights to design or invention are		Lanu			
ļ					reserved.					
ı					PROJECT/PART NUMBER:	REV	PART T	YPE:	DRAWN BY:	
	С	ADD OPERATING TEMPERATURE UPDATE LAIRD LOGO AND REFLOW CURVE			HI2220P171R-10	С	CO-	-FIRE	тмв	
	В	UPDATE COMPANY LOGO ADD ROHS	01/23/08	JRK	DATE: 04/02/04 SI	CALE:	LE: NTS		-	
ı	Α	ORIGINAL DRAFT	04/02/04 TMB CAD #		CAD # TO	TOOL #		1	of 1	
ı	REV	DESCRIPTION	DATE	INT		"	-			